REMARKS

Claims 1-4, 6-8 and 10-18 stand rejected under § 103 on the basis of Yokote and Rege. Applicants respectfully submit that the cited references, alone or in combination, fail to disclose or suggest al of the claimed features of the present invention.

Although Yokote does discloses a memory array, this reference does not disclose a way of possibly accessing a "large scale memory device" as described on page 11, line 18. Yokote does not describe a bus connection scheme that is capable of addressing the inventive memory array 101 described on page 11, line 18 of the present specification.

The Examiner describes "server 14" of Yokote as being equivalent of the "stream server processor" of the present application, but the Yokote server 14 is actually the controlling server, and is not part of the Yokote "output board 58." Moreover, Yokote fails to disclose multiple output stream server processors or an arbitrator required to allow "near simultaneous access," as recited in Claim 1. The Examiner combines Rege to add the arbitrator, but the arbitrator of Rege does not show utility for a RAM system, and introduces latency by using a LAN to control and set up the arbitrator. This prevents the arbitrator of Rege from satisfying the "near simultaneous access" to a "large scale memory device," which are features of Claim 1.

Referring to Claim 4, the bus described in Yokote is not capable of accessing 65 Gbytes of memory.

Referring to Claim 18, the Examiner cites the "memory array address and logic 38" in Yokote as being the same as "CPU running software." To the contrary, a packet with

a "pause" command must be decoded, and acted upon, which is not the simple work of the Yokote "addressing logic." For these reasons, Applicants request withdrawal of this rejection in its entirety.

Claim 5 stands rejected under § 103 on the basis of Yokote, Rege and Thompson. Applicants traverse this rejection for the reasons given with respect to Claims 1 and 3, and the following reason.

The Examiner relies on this combination of references to establish "an address bus greater than 36 bits." However, the Thompson reference only cites a throughput of 267 Mbytes to 1.2 Gbytes of throughput, and 32 Mbytes to 2 Gbytes of capacity. Certainly, the maximum of two Gbytes could be easily satisfied with a simple 32 bit address bus, and nowhere in Thompson does it specify an address bus at all.

The Examiner appears to have confused capacity with throughput. Thompson does not describe anything about a specific size address bus, or a capacity greater than two Gbytes. Accordingly, withdrawal of this rejection is also requested.

Claim 9 stands rejected under § 103 on the basis of Yokote, Rege and Youden.

Applicants traverse this rejection for the reasons given with respect to Claims 3 and 1, and request withdrawal.

Applicants have also added new Claims 23-27. New Claim 23 is an independent claim based on the combination of the subject matter of Claims I and 8, and new Claims 24-27 are dependent claims that refer back to new independent Claim 23. Applicants respectfully submit that new Claim 23 is allowable for at least the reasons discussed above

with regard to independent Claim 1, and also because the cited references do not disclose or

suggest starting a new stream before the entire content of a program is loaded into memory,

as in new independent Claim 23. Yokote does not describe a way to start a new stream

before the entire content of an audio/video program is loaded into memory. Thus, for at least

these additional reasons, Claim 23 and associated dependent Claims 24-27 are also

allowable.

For the foregoing reasons, Applicants believe that this case is in condition for

allowance, which is respectfully requested. The Examiner should call Applicants' attorney if

an interview would expedite prosecution.

Respectfully submitted,

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